



#8/Comdt  
J. Ford  
7/2/03  
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Confirmation No.: 7046

KOMURA et al.

Group Art Unit: 2816

Application No.: 09/921,561

Examiner: Minh T. Nguyen

Filed: August 6, 2001

Attorney Dkt. No.: 024016-00012

For: DELAY CIRCUIT, SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE  
CONTAINING A DELAY CIRCUIT AND DELAY METHOD

**RESPONSE UNDER 37 C.F.R. § 1.111**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Date: June 13, 2003

Sir:

This paper is filed in response to the Office Action dated January 13, 2003, the time period for response having been extended from April 13, 2003 until June 13, 2003 by the enclosed Petition for Extension of Time. In the prosecution of the application identified above, please consider the following:

07/21/2003 AFORD1 00000006 012300 09921561

01 FC:1202

36.00 DA

02 FC:1201

168.00 DA

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JUN 18 2003  
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